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APPLICANTS

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** CONTINUING DATA *****

** FOREIGN APPLICATIONS *****

JAPAN 2003-051360 02/27/2003 *OK* *Yan*

** IF REQUIRED, FOREIGN FILING LICENSE GRANTED **

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35 USC 119(a-d) conditions met	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	<input checked="" type="checkbox"/> Met after Allowance VTan Initials			

ADDRESS

OBLON, SPIVAK, MCCLELLAND MAIER & NEUSTADT, P.C.
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TITLE

Semiconductor apparatus having logic level decision circuit and inter-semiconductor apparatus signal transmission system

FILING FEE RECEIVED 1298	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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